

Project Leader

Role

Project Leader - Technically driving projects to completion with teams of 1-4 people

Experience

5+ years experience in the domain of Interface solutions. Good hands on in modeling and circuit design with top level understanding of layout is a must.

Responsibility

- Block/Top Level modeling/verification of IOs/PHYs
- Circuit Design for Drivers, Receivers etc
- Top Level Floor planning/Layout for the IOs/PHYs
- Project Management
- Customer Communication

Pre Requisites

- Hands on in
 - Interface block/top level modeling
 - CMOS Circuit Design (90nm/65nm/45nm/40nm)
 - Layout and Top Level Floor planning
 - Characterization and Regression of the Modules
- Good knowledge of
 - CMOS IO circuits - LVCMOS, LVDS etc
 - IO Layout and Floor planning
 - Verilog A/MS
 - Skill/Ocean/Virtuoso
 - EM/IR in Layout

Good to have

- ESD Understanding
- SI/System Understanding