

Job title: Senior Digital Backend Engineer

Job Description:

Need to execute the complete digital backend (RTL-to-GDSII) for multi-million gate digital blocks for customers in Deep Sub-micron/Ultra-Deep Sub-micron technology nodes independently. Work will include floorplanning, place and route (P&R) and complete closure of the block for signal, power, design integrity. Timing closure challenges, design closure challenges will be abundant along with the opportunity to work with industry experts on the state-of-the-art processes, tools and flows.

Place of work:

Hubli (*Based on project needs, onsite at Bangalore for short durations might be needed*)

Basic Qualifications needed:

- Independent contributor with good working experience on digital backend and expertise in
 - Floorplanning, timing-driven placement, routing, clock-tree synthesis, tuning, timing closure
 - Signal integrity, power integrity, design integrity checks and closure
- Working experience in scripting languages such as PERL, TCL etc.
- Strong learning, problem solving skills
- 3+ years of relevant experience

Additional Preferred Qualifications:

- Knowledge of Ultra-Deep Submicron (UDSM) issues in digital backend
- Understanding of synthesis, formal verification and Static Timing Analysis (STA)
- Self-driven in learning and execution, Willingness to work hard and strong teaming skills.

Compensation & Benefits:

- Attractive package will be offered as per industry practices.