

Job title: Senior Cell Library Engineer

Job Description:

Need to execute the complete digital cell library (core/IO) from library specifications to library release in Deep Sub-micron/Ultra-Deep Sub-micron processes independently. Work will include library spec development/understanding, layout architecture, schematic design, layout design, characterization/dotlib generation, cad modelling, library modelling/packaging, QC and release. Key challenges will be low area, power along with exceptional quality and cycletime. This job will include working with industry experts on the state-of-the-art processes, tools, flows and methodologies.

Place of work:

Hubli *(Based on project needs, onsite at Bangalore for short durations might be needed)*

Basic Qualifications needed:

- Bachelor's/Master's degree in Engineering (EEE, ECE, E&I) with 2+ years of relevant experience
- Independent contributor with good working experience on digital cell library development and expertise in various aspects of library creation
 - Layout architecture/design, Schematic design, characterization/dotlib generation, cad modelling, library modelling/packaging, QC and release
- Cadence DFII environment and programming in SKILL
- Working experience in scripting languages such as PERL, TCL etc.
- Strong learning, problem solving skills

Additional Preferred Qualifications:

- Knowledge of low-area, low-power techniques.
- Understanding of digital backend and how libraries fit into the backend flow.
- Self-driven in learning and execution, Willingness to work hard and strong teaming skills.

Compensation & Benefits:

- Attractive package will be offered as per industry practices.