

Senior Mixed Signal Verification Engineer

Job Description

You will be working on top level verification of complex mixed signal/RF designs. The work will involve design and setup of verification environment and require closely working with RTL/circuit designers to define, develop, maintain and run mixed signal top level test suites.

Key Skills

- Candidate should have hands on experience on digital and mixed signal top level verification, RTL design/verification, modeling of analog circuits and good understanding of top level and mixed signal interface/verification issues.
- Working experience in modeling languages such as VHDL, VHDL-AMS , verilog-AMS and scripting languages such as PERL, TCL etc.
- Basic understanding of analog circuits
- Modeling of analog circuits
- Familiarity with Cadence/Mentor circuit schematic tools
- Experience with spice and/or fastspice simulators e.g - Hspice, spectre,Hsim, Nanosim, Finesim.
- Knowledge of issues in mixed signal verification
- Experience in top level verification environment setup
- Understanding of RTL design/verification concepts

Education/Experience

- Bachelor's degree in Electronics.
- 3+ years of relevant experience.
- Worked on mixed signal simulators such AMS designer, Questa ADMS.

Expectations

- Be able to work in a start-up environment.
- Self-driven in learning and execution.
- Willingness to work hard and strong teaming skills.
- Good interpersonal skills.

Location will be Hubli in Karnataka (Based on project needs, onsite at Bangalore for short durations)

Compensation & Benefits:

Attractive package will be offered as per industry practices.